

Multi-Candela Circuitry Block Diagram

10

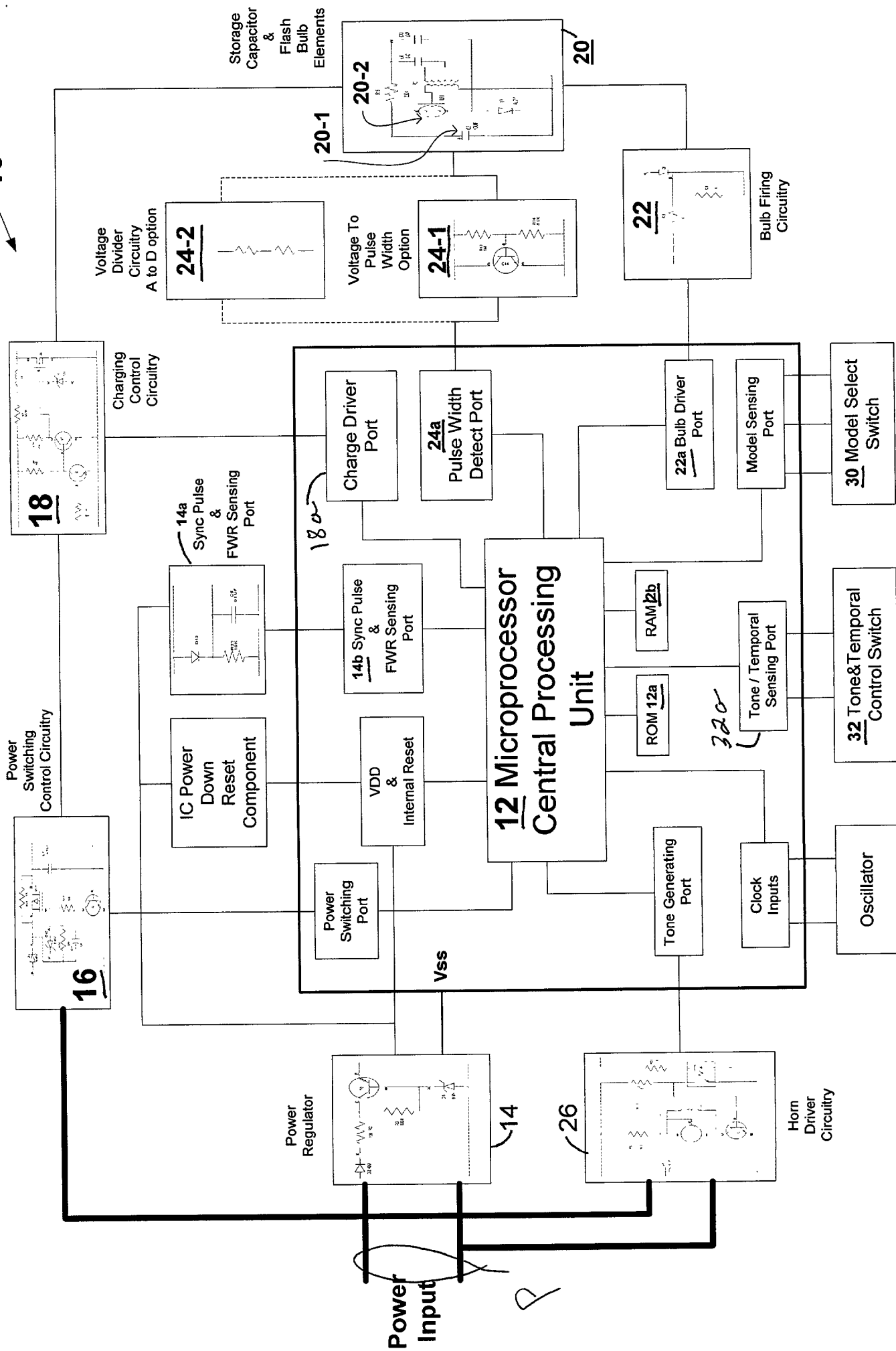


Fig. 1

CONTROL PROCESS

Pulse Width Option

Analog to Digital Option

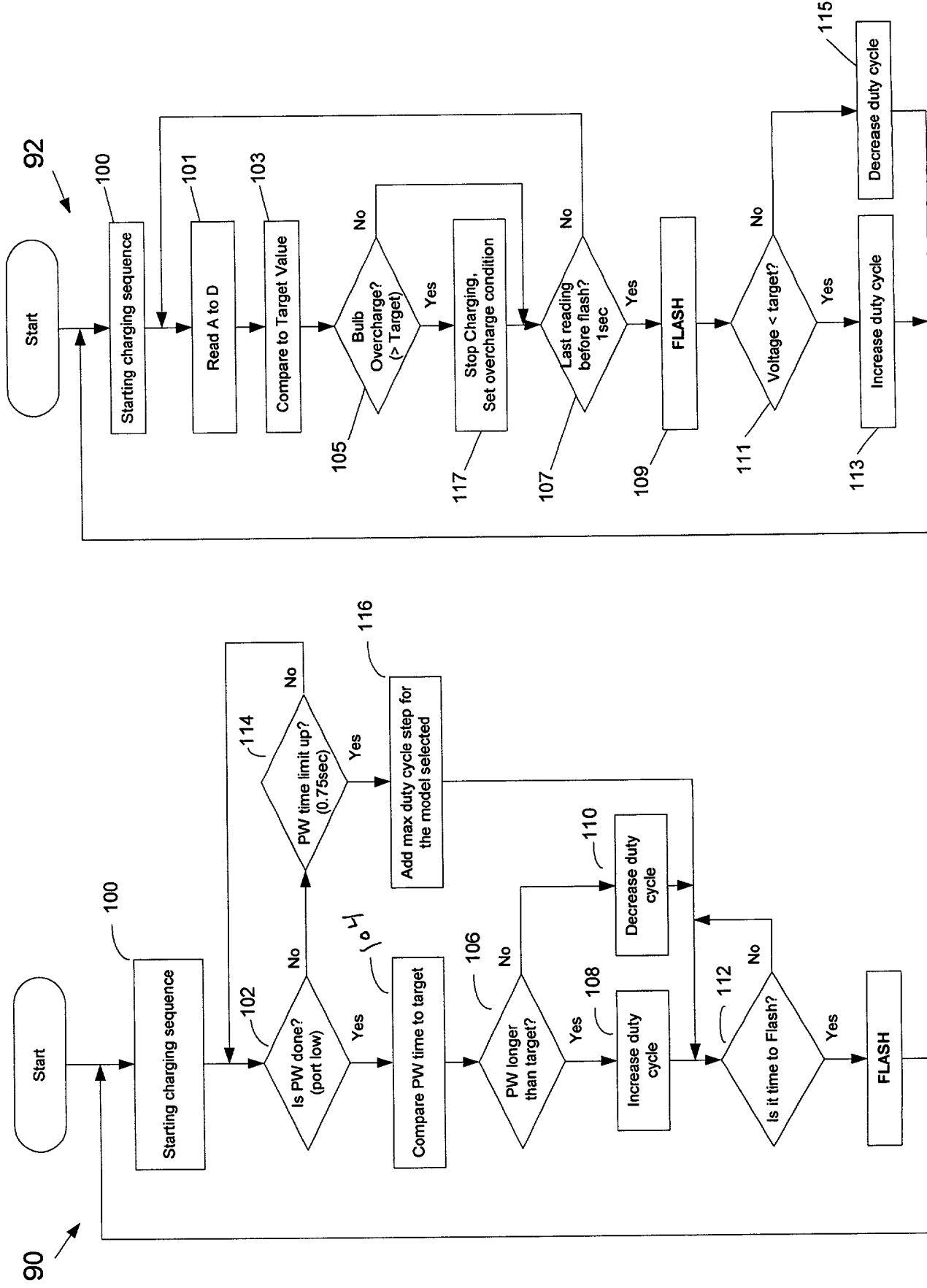


Fig. 2A-1

Fig. 2A-2

Detail Operation Of Candela Searching

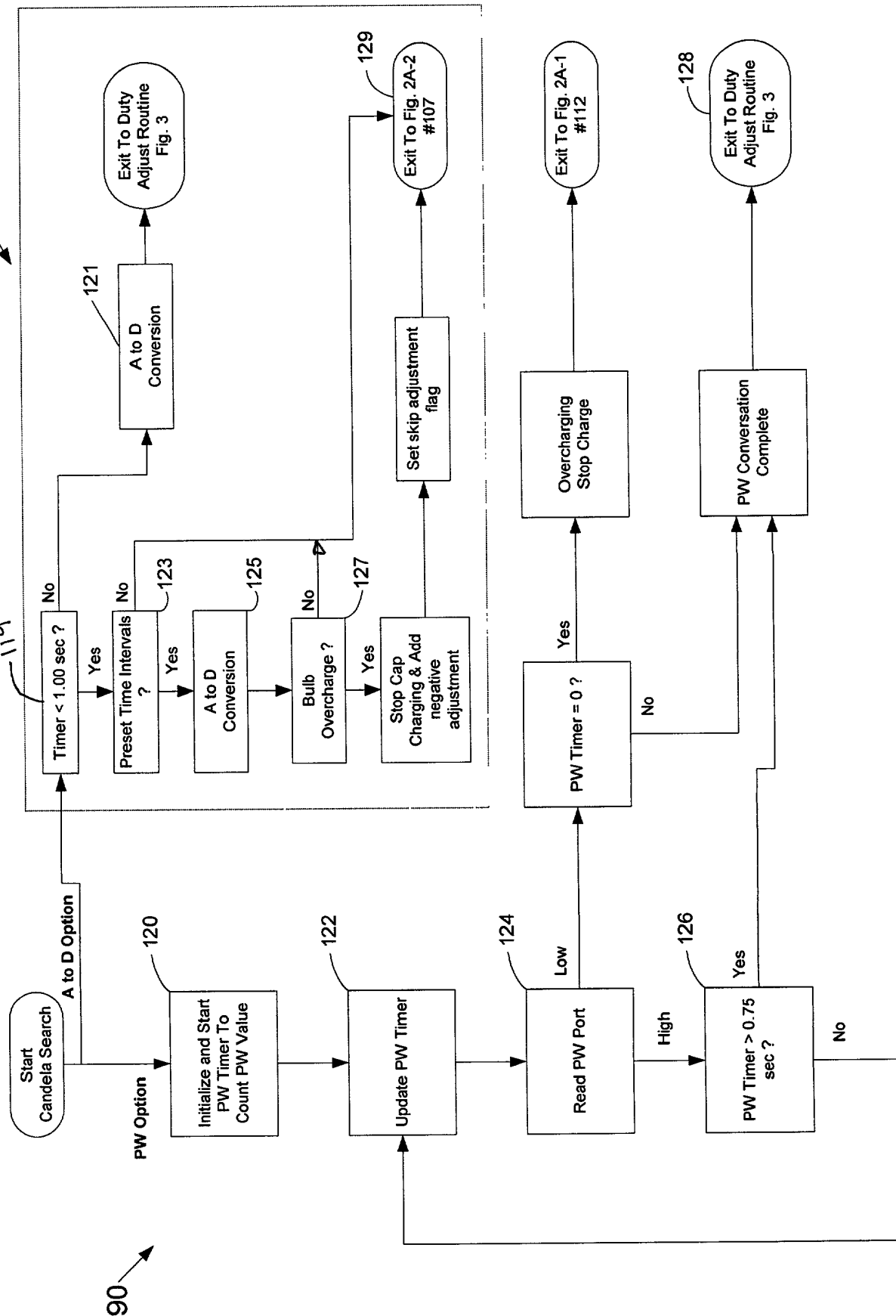


Fig. 2B

Detail Operation Of Candela Searching

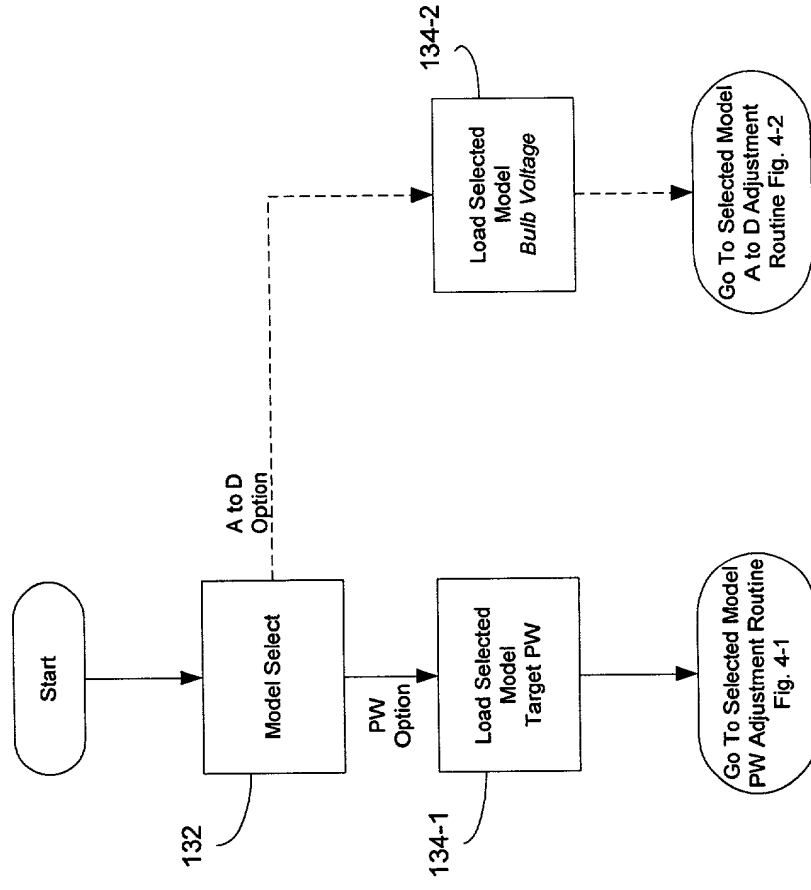


Fig. 3

All Models **Detail Operation Of Candela Searching** **PW Option**

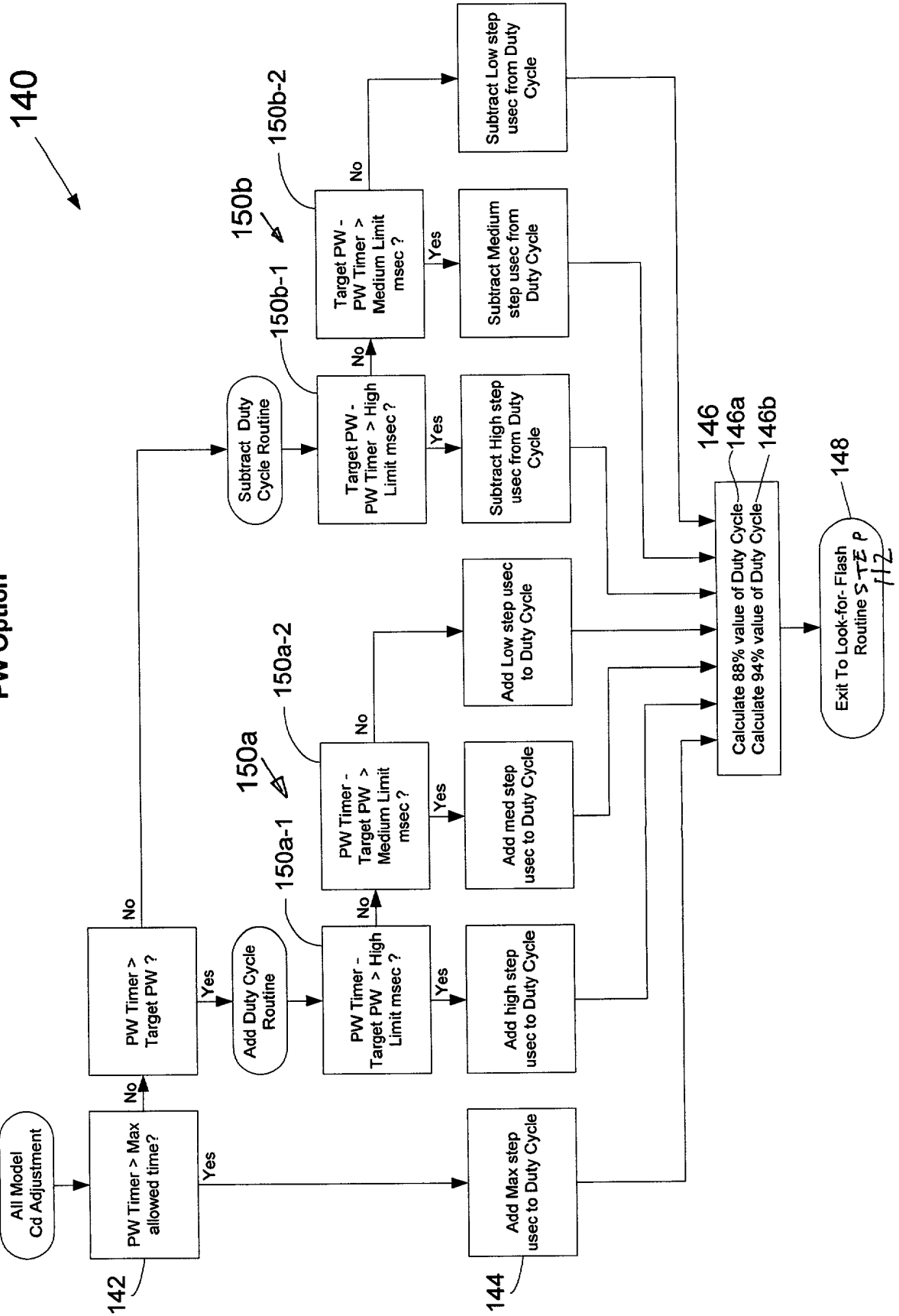


Fig. 4-1

Detail Operation Of Candela Searching A to D Option

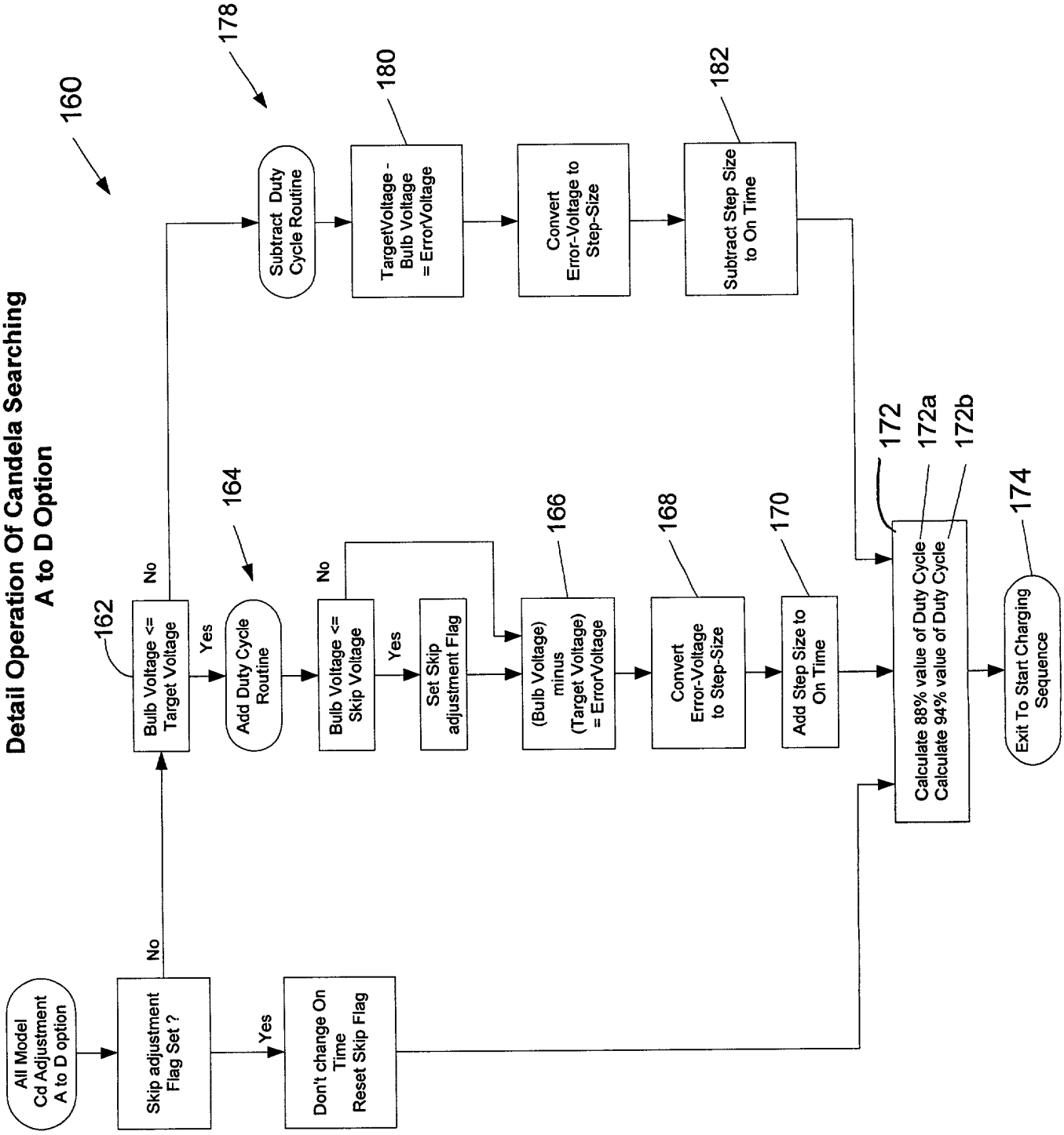


Fig. 4-2

Pulse Width Option: Candela Target Searching Timing Diagrams for raising bulb voltage to target

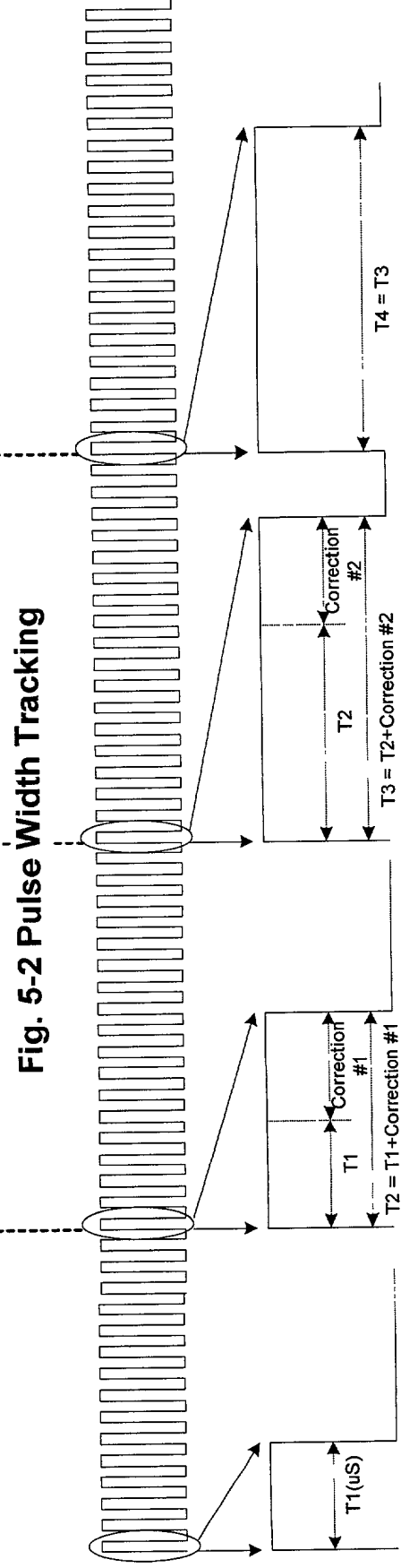
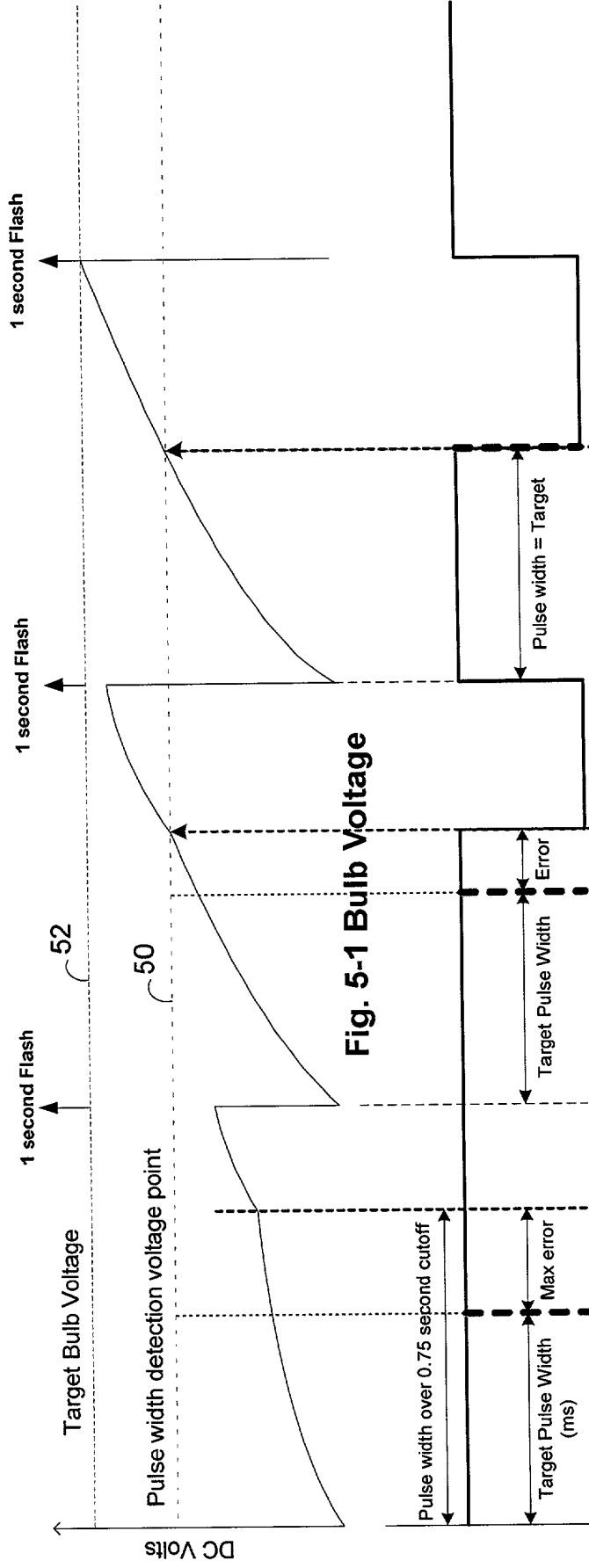


Fig. 5-3 Charging Driver Signal
note: wave form expanded to show detail

Pulse Width Option: Candela Target Searching Timing Diagrams for lowering bulb voltage to target

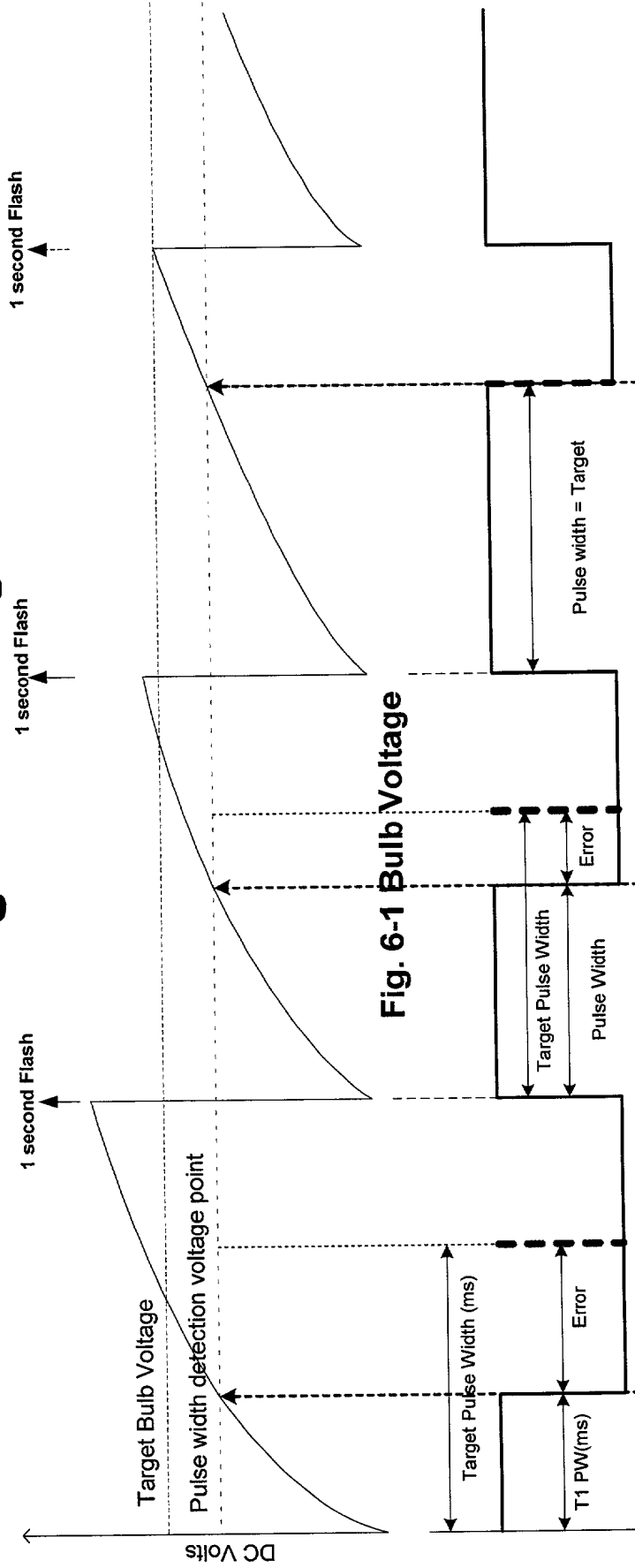
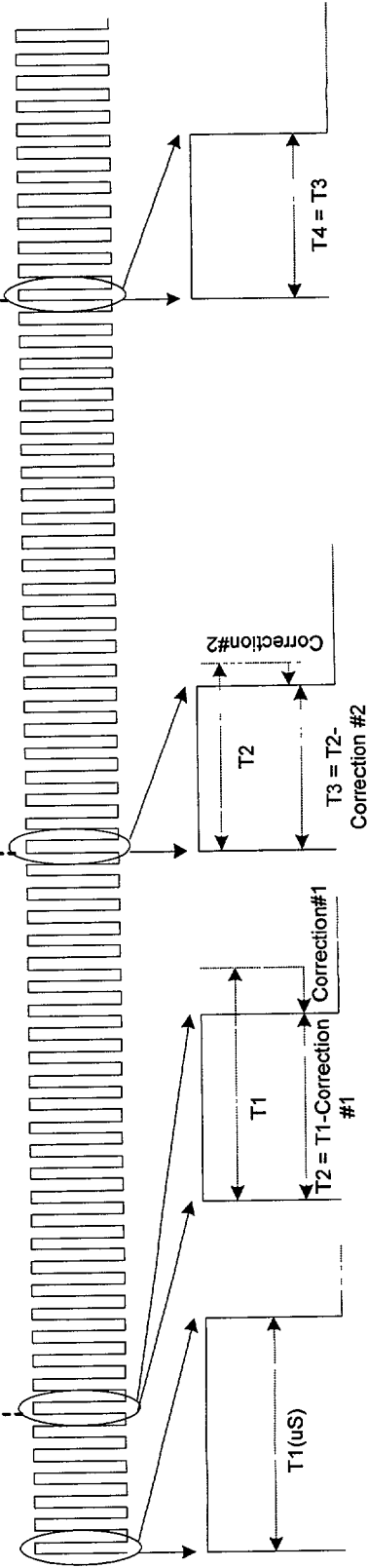


Fig. 6-2 Pulse Width Tracking



Analog to Digital option: Candela Target Searching Timing Diagrams for raising bulb voltage to target

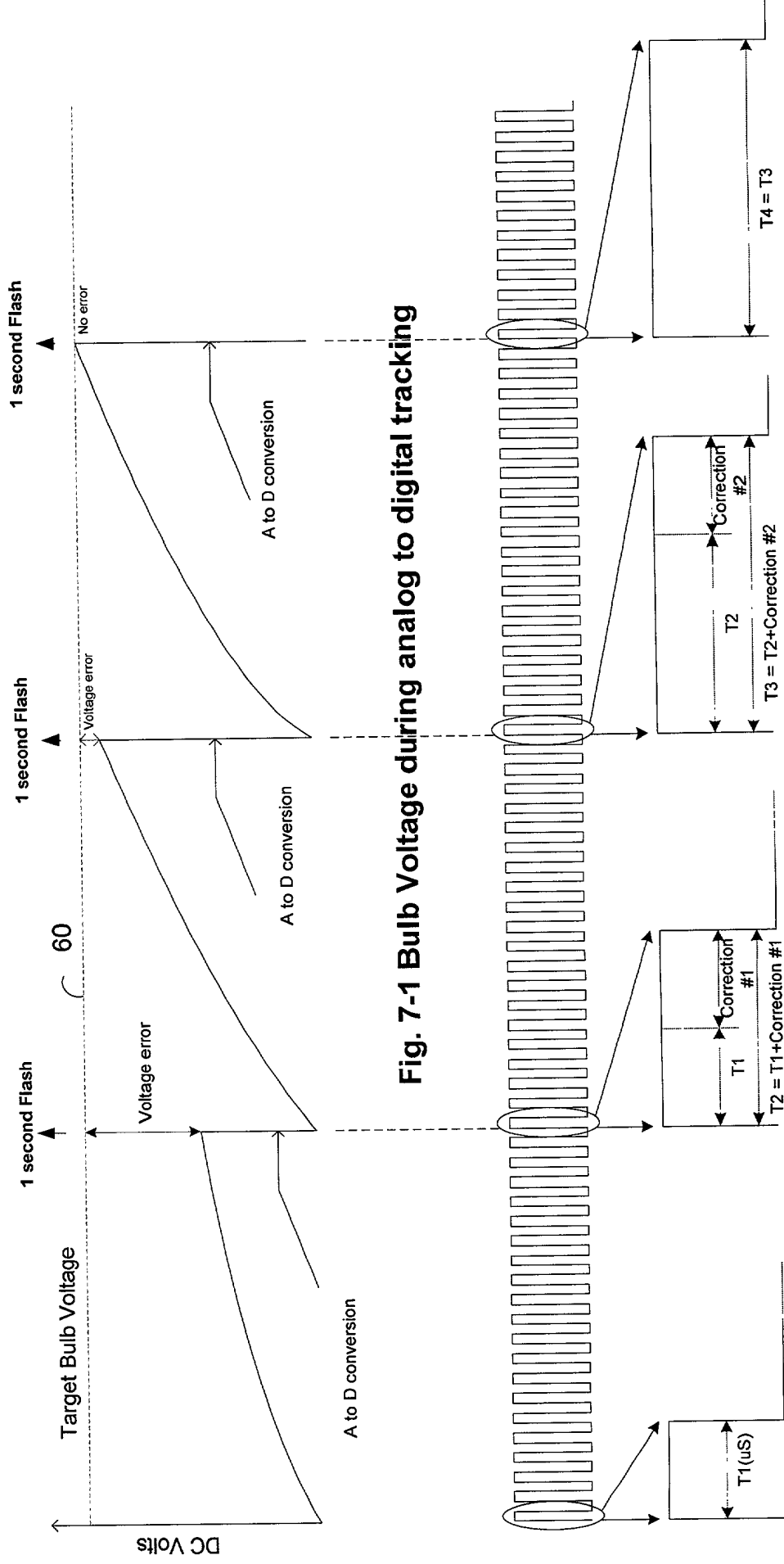


Fig. 7-1 Bulb Voltage during analog to digital tracking

Fig. 7-2 Charging Driver Signal
note: wave form expanded to show detail

Analog to Digital Option: Candela Target Searching Timing Diagrams for lowering bulb voltage to target

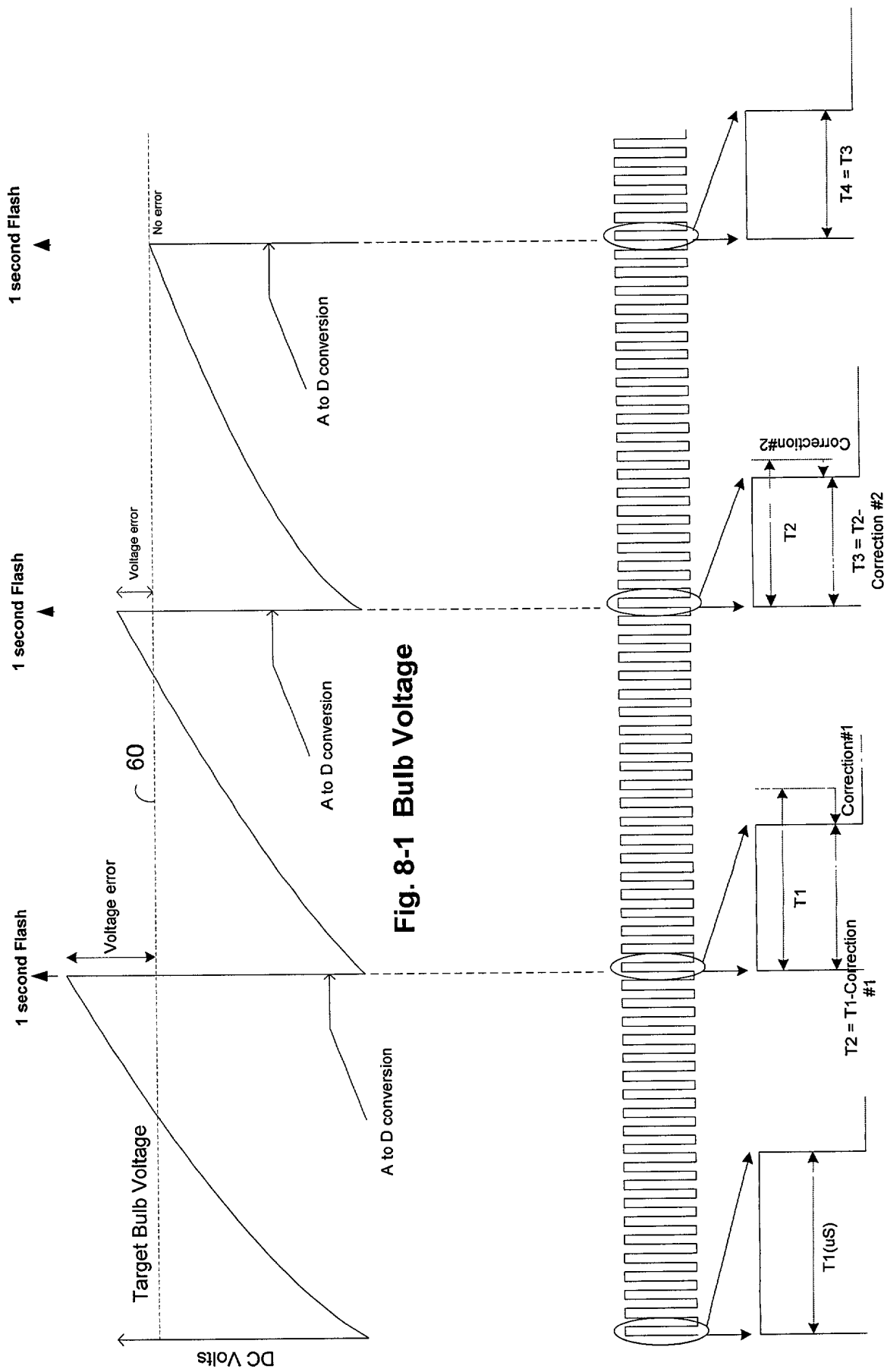
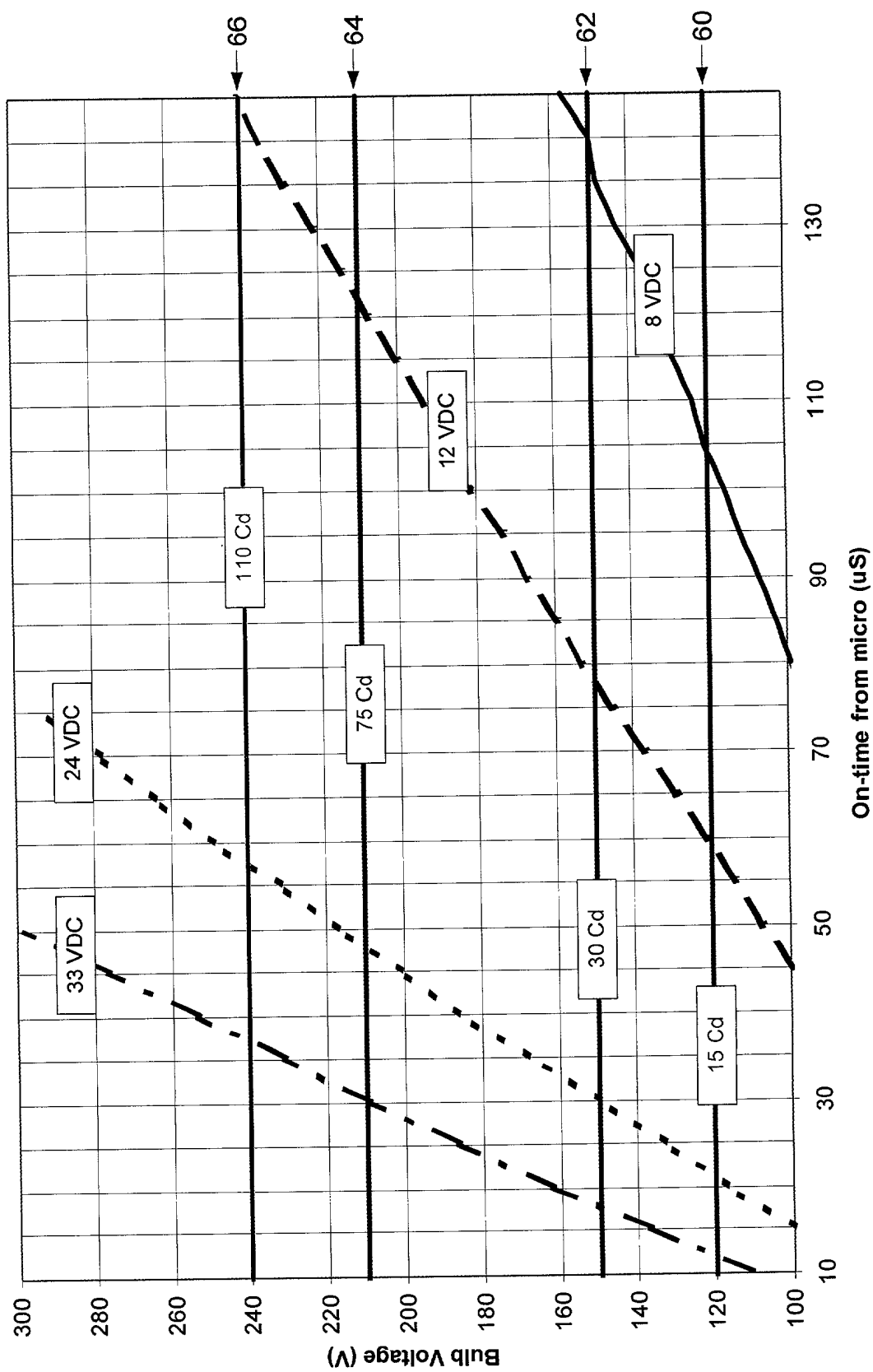


Fig. 8-2 Charging Driver Signal
 note: wave form expanded to show detail



**Fig 9. MultiCandela Bulb Voltage vs On-time
for different input voltage**

Multi Candela Timing Reference For Inrush Control And Candela Searching

Bulb Capacitor Voltage Waveform

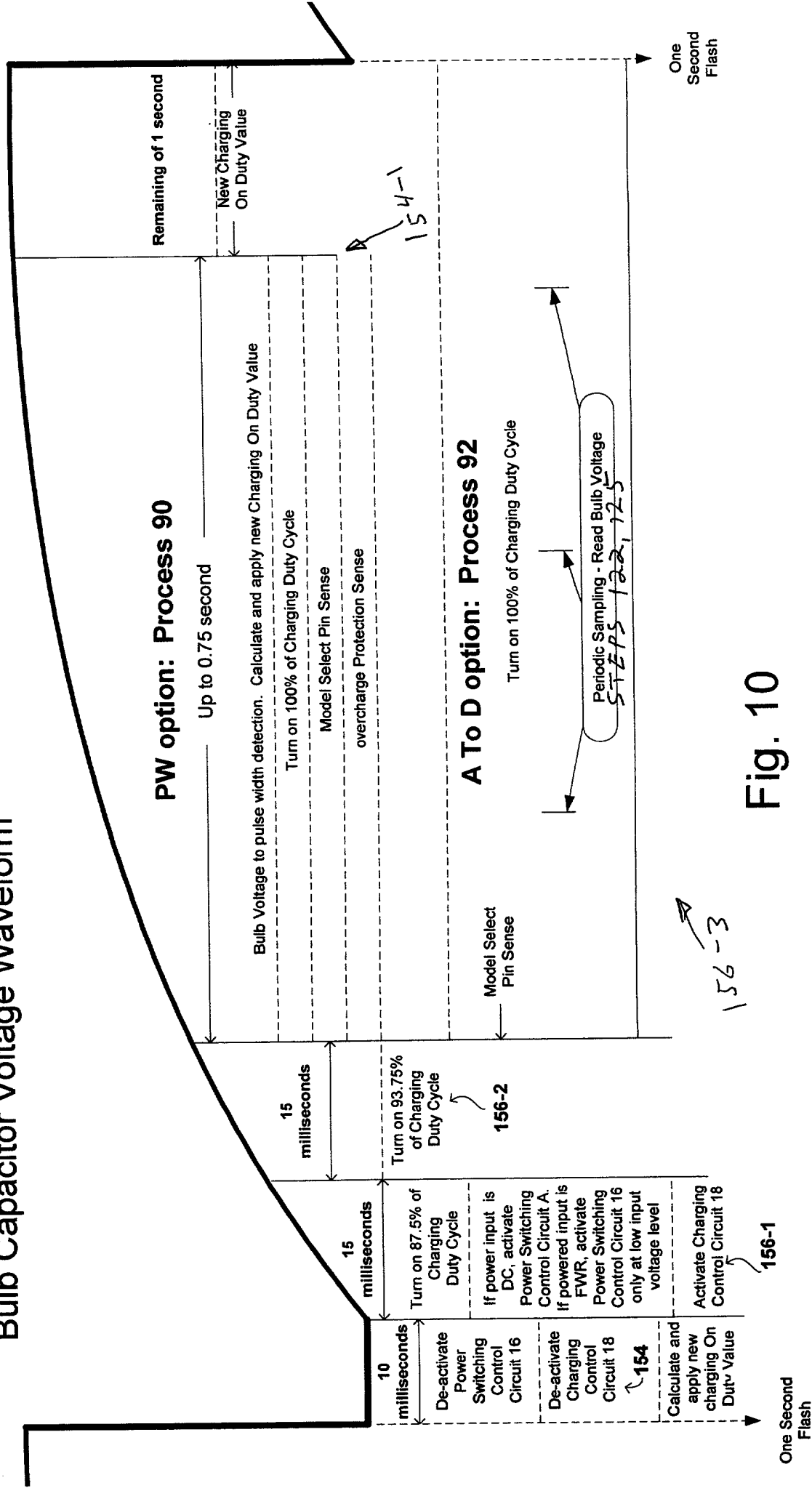


Fig. 10

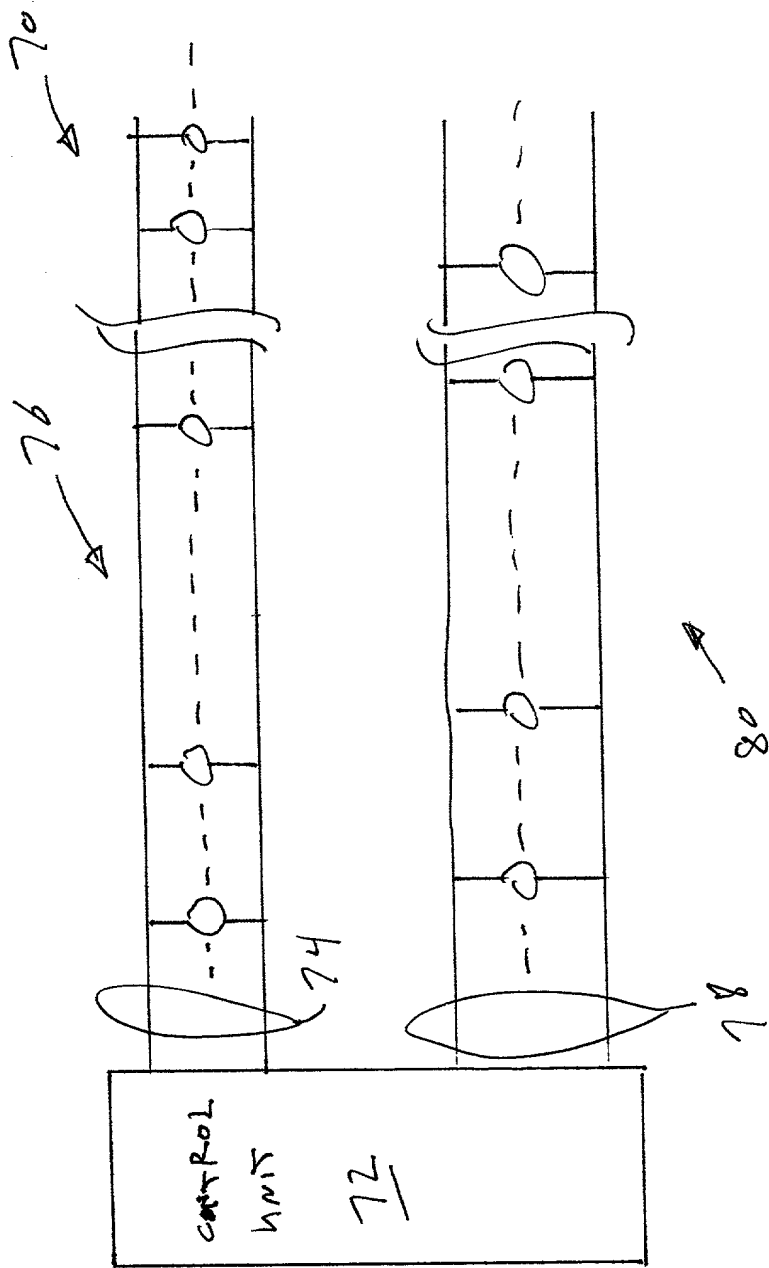


FIG. 11